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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lin, *et al.* Docket No.: TSM03-0670
Serial No.: 10/729,092 Art Unit: 2811
Filed: December 5, 2003 Examiner: TBD
For: Structure and Method of Forming Integrated Circuits Utilizing Strained
Channel Transistors

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Form PTO/SB/08A 1 Reference Cited (1 page)
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Respectfully submitted,

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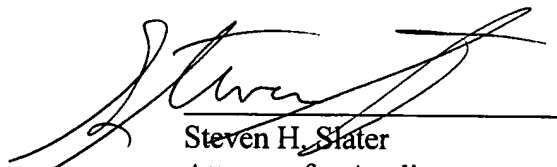
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The Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application.

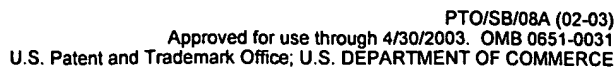
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Respectfully submitted,

June 10, 2004
Date


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Substitute for form 1449/PTO

(use as many sheets as necessary)

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of

1

Complete if Known

Application Number

10729.092

Filing Date

December 5, 2003

First Named Inventor

Lin, et al.

Art Unit

2811

Examiner Name

TBD

Attorney Docket Number

TSM03-0670

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

**Examiner
Signature**

Date _____

Considered

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